Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	95	ECC$1 and (replacement adj processing)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 11:44
L2	82733	("707"/$.ccls. or "714"/$.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 11:34
L3	15070	L2 and (convert$6 or transform$6 or conversion or chang$4) near9 (file or type or mode)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 11:33
L4	42	1 and (convert$6 or transform$6 or conversion or chang$4) near9 (file or type or mode)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 11:41
L5	2	4 and ("707"/$.ccls. or "714"/$.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 11:34
L6	1	4 and (714/710.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 11:35
L7	469	(714/710.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 11:35
L8	12	7 and (replacement adj processing)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 11:37
L9	11	8 and error and area and (read or reading) and (writing or write or written or wrote)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 11:40
L10	84	1 and error and area and (read or reading) and (writing or write or written or wrote)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 11:40

L11	71	1 and error and area and (read or reading) and (writing or write or written or wrote) and sector	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 11:40
L12	41	11 and (convert$6 or transform$6 or conversion or chang$4) near9 (file or type or mode)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 11:41
L13	25	11 and (convert$6 or conversion or chang$4) near4 (file or type or mode)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 11:44
L14	103	((error adj correction adj code) or ECC$1) and (replacement adj processing)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 11:44
L15	21	14 and av adj (file or mode)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 11:45
L16	2	15 and dvd-ram and driver	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 11:46
L17	2	15 and driver	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 11:46

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1637	(((error adj correction adj code) or ECC$1) or (replacement adj processing)).ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 12:49
L2	0	(((error adj correction adj code) or ECC$1) and (replacement adj processing)).ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 12:58
L3	0	1 and (((error adj correction adj code) or ECC$1) and (replacement adj processing)).ab.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 12:44
L4	1058	1 and (((error adj correction adj code) or ECC$1) or (replacement adj processing)).ab.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 12:45
L5	2	4 and file adj6 (mode or type)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 12:44
L6	22	1 and (replacement adj processing). ab.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 12:47
L7	14	1 and (replacement adj processing) and error	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 12:47
L8	0	1 and (replacement adj processing) and error and area and mode	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 12:54
L9	64	(replac$6 near9 (error and area and mode))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 12:48
L10	0	1 and 9	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 12:48

L11	23	9 and (((error adj correction adj code) or ECC$1) or (replacement adj processing))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 12:51
L12	1	11 and file adj (type or mode)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 12:53
L13	1	9 and ((file adj (type or mode)) or ((av or pc) adj (file or mode)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 12:54
L14	10549	((file adj (type or mode)) or ((av or pc) adj (file or mode)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 13:00
L15	6	14 and (replacement adj processing) and error and area and mode	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 12:58
L16	110	(replacement adj processing) and error and area and mode	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 12:58
L17	49	16 and (((error adj correction adj code) or ECC$1) and (replacement adj processing))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 13:01
L18	5	17 and ((av or pc) adj (file or mode))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 12:59
L19	6	17 and ((file adj (type or mode)) or ((av or pc) adj (file or mode)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 13:01
L20	15968	((file adj2 (type or mode)) or ((av or pc) adj (file or mode)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 13:19

L21	22	20 and (((error adj correction adj code) or ECC$1) and (replacement adj processing))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 13:08
L22	3211	20 and ("707"/$.ccls. or "714"/$.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 13:08
L23	96	22 and (((error adj correction adj code) or ECC$1) or (replacement adj processing))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 13:20
L24	68	23 and sector	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 13:20
L25	67	24 and (read or reading) and writ$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 13:17
L26	65	24 and (read and reading) and writ$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 13:10
L27	59	24 and (read and reading) and write and writing and written	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 13:21
L28	20399	((file adj2 (type or mode)) or ((av or pc) adj (file or mode or data)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 13:19
L29	25659	((file adj2 (type or mode)) or ((av or pc) adj2 (file or mode or data)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 13:19
L30	26	28 and (((error adj correction adj code) or ECC$1) and (replacement adj processing))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 13:20

L31	23	30 and sector	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 13:20
L32	20	31 and (read and reading) and write and writing and written	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 13:23
L33	25	30 and (convert$6 or transform$6 or conversion or chang$5) near6 (data or information or av or pc or mode or file or type)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 13:24

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	63	ECC$1 and error and (replacement adj processing) and recording and recorded and ((recording adj area) or sector)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 10:49
L2	6	1 and (dvd-ram and driver)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 10:42
L3	63	1 and (convert$6 or conversion or transform$6 or translat$6 or replac$9)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 10:44
L4	45	1 and (convert$6 or conversion or transform$6 or translat$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 10:50
L5	1	1 and (pc and av) adj2 (type or mode)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 10:45
L6	20	(pc and av) adj2 (type or mode)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 10:45
L7	0	(pc and av) adj2 (type and mode)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 10:45
L9	17	(pc and av) adj2 (file\ and mode)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 10:45
L10	1	(pc and av) adj2 (file and mode)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 10:45
L11	17	(pc and av) adj2 mode	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 10:46

L12	91	ECC$1 and error and (replacement adj processing)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 10:54
L13	11	12 and (convert$6 or conversion or transform$6 or translat$6) with file	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 10:52
L14	17253	(error adj correction adj code)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 10:59
L15	7131	14 and error with (read or reading or writ$4 or written)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 10:53
L16	1182	14 and (data adj error) with (read or reading or writ$4 or written)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 10:54
L17	597	14 and (data adj error) adj9 (read or reading or writ$4 or written)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 11:01
L18	12	17 and ECC$1 and error and (replacement adj processing)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 10:58
L19	10	18 and file	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 10:56
L20	10	18 and file and type	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 10:56
L21	15	14 and ECC$1 same error same (replacement adj processing)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 10:58

L22	20	14 and ((error adj correction adj code) or ECC$1) same error same (replacement adj processing)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 10:59
L23	13	22 and (data adj error) and (read or reading) and (writ$4 or written)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 11:03
L24	13	23 and file and dvd-ram	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 11:03
S1	1087	(ECC adj blocks!)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 10:30
S2	197	S1 and ("707"/$.ccls. or "714"/$.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/01 13:50
S3	22	S2 and file adj type	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/01 13:42
S4	1570	(ECC adj block) and sector	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/01 13:49
S5	1	S4 and (convert$6 or transform$6 or conversion) with (file adj3 (type or mode))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/01 13:48
S6	1	S1 and (convert$6 or transform$6 or conversion) with (file adj3 (type or mode))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/01 13:48
S7	812	(convert$6 or transform$6 or conversion) with (file adj3 (type or mode))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/01 13:50

S8	152	S7 and ("707"/$.ccls. or "714"/$.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/01 13:49
S9	1	S8 and ECC and sector	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/01 13:49
S10	3	S8 and ECC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/01 13:49
S11	82566	("707"/$.ccls. or "714"/$.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/01 13:50
S12	152	S11 and (convert$6 or transform$6 or conversion) with (file adj3 (type or mode))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/01 13:51
S13	17	S12 and recording adj medium	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/01 15:43
S14	2	"5270957".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/01 15:44
S15	10	("20030120593" or "20040064443" or "6208990" "6189004" or "6151608").pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 06:30
S16	2	"5270957".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 06:50
S17	2	S16 and distribution	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 06:51

S18	2	S16 and distribution and replac$8	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 06:57
S19	2	S16 and distribution and replac$8 and probabilit$7	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 07:04
S20	1	S16 and binomial	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 07:06
S21	0	S16 and (weight$4 or scor$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 07:06

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	63	ECC$1 and error and (replacement adj processing) and recording and recorded and ((recording adj area) or sector)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 10:49
L2	6	1 and (dvd-ram and driver)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 10:42
L3	63	1 and (convert$6 or conversion or transform$6 or translat$6 or replac$9)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 10:44
L4	45	1 and (convert$6 or conversion or transform$6 or translat$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 10:50
L5	1	1 and (pc and av) adj2 (type or mode)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 10:45
L6	20	(pc and av) adj2 (type or mode)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 10:45
L7	0	(pc and av) adj2 (type and mode)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 10:45
L9	17	(pc and av) adj2 (file\ and mode)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 10:45
L10	1	(pc and av) adj2 (file and mode)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 10:45
L11	17	(pc and av) adj2 mode	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 10:46

L12	91	ECC$1 and error and (replacement adj processing)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 10:54
L13	11	12 and (convert$6 or conversion or transform$6 or translat$6) with file	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 10:52
L14	17253	(error adj correction adj code)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 10:59
L15	7131	14 and error with (read or reading or writ$4 or written)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 10:53
L16	1182	14 and (data adj error) with (read or reading or writ$4 or written)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 10:54
L17	597	14 and (data adj error) adj9 (read or reading or writ$4 or written)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 11:01
L18	12	17 and ECC$1 and error and (replacement adj processing)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 10:58
L19	10	18 and file	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 10:56
L20	10	18 and file and type	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 10:56
L21	15	14 and ECC$1 same error same (replacement adj processing)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 10:58

L22	20	14 and ((error adj correction adj code) or ECC$1) same error same (replacement adj processing)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 10:59
L23	13	22 and (data adj error) and (read or reading) and (writ$4 or written)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 11:03
L24	13	23 and file and dvd-ram	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 11:03

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	95	ECC$1 and (replacement adj processing)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 11:35
L2	82733	("707"/$.ccls. or "714"/$.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 11:34
L3	15070	L2 and (convert$6 or transform$6 or conversion or chang$4) near9 (file or type or mode)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 11:33
L4	42	1 and (convert$6 or transform$6 or conversion or chang$4) near9 (file or type or mode)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 11:34
L5	2	4 and ("707"/$.ccls. or "714"/$.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 11:34
L6	1	4 and (714/710.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 11:35
L7	469	(714/710.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 11:35
L8	12	7 and (replacement adj processing)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 11:37
L9	11	8 and error and area and (read or reading) and (writing or write or written or wrote)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/02 11:38

P❀RTAL

USPTO

THE ACM DIGITAL LIBRARY

🔖 Feedback Report a problem Satisfaction survey

Terms used **replacement processing error ecc**   Found **61** of **169,866**

Sort results by   relevance ▿   💾 Save results to a Binder   Try an Advanced Search
Display results   expanded form ▿   🔲 Search Tips   Try this search in The ACM Guide
                                            ☐ Open results in a new window

Results 1 - 20 of 61   Result page: **1** 2 3 4   next

Relevance scale ☐ ◲ ◧ ◨ ■

1 Features: Self-healing in modern operating systems   ■
Michael W. Shapiro
December 2004 **Queue**, Volume 2 Issue 9
Publisher: ACM Press
Full text available: 📄 pdf(872.84 KB)   Additional Information: full citation, abstract, references, index terms
                     📄 html(32.65 KB)

A few early steps show there's a long (and bumpy) road ahead.

2 The Wisconsin Wind Tunnel: virtual prototyping of parallel computers   ■
Steven K. Reinhardt, Mark D. Hill, James R. Larus, Alvin R. Lebeck, James C. Lewis, David A. Wood
June 1993 **ACM SIGMETRICS Performance Evaluation Review , Proceedings of the
1993 ACM SIGMETRICS conference on Measurement and modeling of
computer systems SIGMETRICS '93**, Volume 21 Issue 1
Publisher: ACM Press

Full text available: 📄 pdf(1.40 MB)   Additional Information: full citation, abstract, references, citings, index terms

We have developed a new technique for evaluating cache coherent, shared-memory
computers. The Wisconsin Wind Tunnel (WWT) runs a parallel shared-memory program
on a parallel computer (CM-5) and uses execution-driven, distributed, discrete-event
simulation to accurately calculate program execution time. WWT is a virtual prototype
that exploits similarities between the system under design (the target) and an existing
evaluation platform (the host). The host directly executes all target program ins ...

3 Trap-driven memory simulation with Tapeworm II   ■
Richard Uhlig, David Nagle, Trevor Mudge, Stuart Sechrest
January 1997 **ACM Transactions on Modeling and Computer Simulation (TOMACS)**,
Volume 7 Issue 1
Publisher: ACM Press
Full text available: 📄 pdf(630.91 KB)   Additional Information: full citation, references, index terms

Keywords: Cache, TLB, memory system, simulation, trace-driven simulation, trap-driven
simulation

4 Trace-driven memory simulation: a survey

Richard A. Uhlig, Trevor N. Mudge

June 1997 **ACM Computing Surveys (CSUR)**, Volume 29 Issue 2

Publisher: ACM Press

Full text available: pdf(636.11 KB)

Additional Information: full citation, abstract, references, citings, index terms, review

As the gap between processor and memory speeds continues to widen, methods for evaluating memory system designs before they are implemented in hardware are becoming increasingly important. One such method, trace-driven memory simulation, has been the subject of intense interest among researchers and has, as a result, enjoyed rapid development and substantial improvements during the past decade. This article surveys and analyzes these developments by establishing criteria for evaluating trac ...

Keywords: TLBs, caches, memory management, memory simulation, trace-driven simulation

5 Parameter replacement for CELP coded speech in land mobile radio

Yaacov Yesha

July 1998 **Wireless Networks**, Volume 4 Issue 4

Publisher: Kluwer Academic Publishers

Full text available: pdf(90.72 KB)   Additional Information: full citation, abstract, references, index terms

The contribution of this paper is in applying parameter replacement techniques to speech that is compressed by the Federal Standard 1016 CELP speech coder, protected by Reed-Solomon codes, and transmitted over a wireless channel. The parameter replacement results in significant improvement in speech quality without any increase in bit rate.

6 Assessing Fault Sensitivity in MPI Applications

Charng-da Lu, Daniel A. Reed

November 2004 **Proceedings of the 2004 ACM/IEEE conference on Supercomputing**

Publisher: IEEE Computer Society

Full text available: pdf(678.48 KB)   Additional Information: full citation, abstract

Today, clusters built from commodity PCs dominate high-performance computing, with systems containing thousands of processors now being deployed. As node counts for multi-teraflop systems grow to thousands and with proposed petaflop system likely to contain tens of thousands of nodes, the standard assumption that system hardware and software are fully reliable becomes much less credible. Concomitantly, understanding application sensitivity to system failures is critical to establishing confidenc ...

7 Trap-driven simulation with Tapeworm II

Richard Uhlig, David Nagle, Trevor Mudge, Stuart Sechrest

November 1994 **ACM SIGPLAN Notices , ACM SIGOPS Operating Systems Review , Proceedings of the sixth international conference on Architectural support for programming languages and operating systems ASPLOS-VI**, Volume 29 , 28 Issue 11 , 5

Publisher: ACM Press

Full text available: pdf(1.45 MB)

Additional Information: full citation, abstract, references, citings, index terms

Tapeworm II is a software-based simulation tool that evaluates the cache and TLB performance of multiple-task and operating system intensive workloads. Tapeworm resides in an OS kernel and causes a host machine's hardware to drive simulations with kernel traps instead of with address traces, as is conventionally done. This allows Tapeworm to quickly and accurately capture complete memory referencing behavior with

a limited degradation in overall system performance. This paper compares trap- ...

Keywords: TLB, cache, memory system, trace-driven simulation, trap-driven simulation

8 Active memory: a new abstraction for memory system simulation

Alvin R. Lebeck, David A. Wood
January 1997 **ACM Transactions on Modeling and Computer Simulation (TOMACS)**,
Volume 7 Issue 1
Publisher: ACM Press
Full text available: pdf(690.38 KB)   Additional Information: full citation, references, citings, index terms

Keywords: Cache memory, direct-execution simulation, memory hierarchy, on-the-fly simulation, trace-driven simulation

9 Survey of software tools for evaluating reliability, availability, and serviceability

Allen M. Johnson, Miroslaw Malek
September 1988 **ACM Computing Surveys (CSUR)**, Volume 20 Issue 4
Publisher: ACM Press

Full text available: pdf(3.79 MB)   Additional Information: full citation, abstract, references, citings, index terms

In computer design, it is essential to know the effectiveness of different design options in improving performance and dependability. Various software tools have been created to evaluate these parameters, applying both analytic and simulation techniques, and this paper reviews those related primarily to reliability, availability, and serviceability. The purpose, type of models used, type of systems modeled, inputs, and outputs are given for each package. Examples of some of the key modeling ...

10 Selective-set-invalidation (SSI) for soft-error-resilient cache architecture

Seung H. Hwang, Gwan S. Choi
June 1999 **ACM SIGARCH Computer Architecture News**, Volume 27 Issue 3
Publisher: ACM Press
Full text available: pdf(489.77 KB)   Additional Information: full citation, abstract, index terms

This paper proposes a novel cache-memory design for soft-error silence, and verifies the design through a simulation that uses realistic system and software model. The SSI design is a combination of an n-bit error detector and a fast circuit that allows real-time-forced invalidation of corrupted data sets. The current design supports the write through caching policy and will be extended for the write back policy. To verify the effectiveness of the proposed design approach, mixed-mode simulations ...

11 Area efficient architectures for information integrity in cache memories

Seongwoo Kim, Arun K. Somani
May 1999 **ACM SIGARCH Computer Architecture News , Proceedings of the 26th annual international symposium on Computer architecture ISCA '99**, Volume 27 Issue 2
Publisher: IEEE Computer Society, ACM Press
Full text available: pdf(227.09 KB)   Additional Information: full citation, abstract, references, citings, index terms
Publisher Site

Information integrity in cache memories is a fundamental requirement for dependable computing. Conventional architectures for enhancing cache reliability using check codes make it difficult to trade between the level of data integrity and the chip area

requirement. We focus on transient fault tolerance in primary cache memories and develop new architectural solutions, to maximize fault coverage when the budgeted silicon area is not sufficient for the conventional configuration of an error checki ...

12 <u>RAID: high-performance, reliable secondary storage</u>

Peter M. Chen, Edward K. Lee, Garth A. Gibson, Randy H. Katz, David A. Patterson
June 1994 **ACM Computing Surveys (CSUR)**, Volume 26 Issue 2
Publisher: ACM Press

Full text available: pdf(3.60 MB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>, <u>review</u>

Disk arrays were proposed in the 1980s as a way to use parallelism between multiple disks to improve aggregate I/O performance. Today they appear in the product lines of most major computer manufacturers. This article gives a comprehensive overview of disk arrays and provides a framework in which to organize current and future work. First, the article introduces disk technology and reviews the driving forces that have popularized disk arrays: performance and reliability. It discusses the tw ...

Keywords: RAID, disk array, parallel I/O, redundancy, storage, striping

13 <u>Evolutionary computation and optimization (ECO): Solving the error correcting code problem with parallel hybrid heuristics</u>

Enrique Alba, J. Francisco Chicano
March 2004 **Proceedings of the 2004 ACM symposium on Applied computing**
Publisher: ACM Press
Full text available: pdf(289.83 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>index terms</u>

Some telecommunication systems can not afford the cost of repeating a corrupted message. Instead, the message should be somewhat "corrected" by the receiver. In these cases an *error correcting code* is suitable. The problem of finding an error correcting code of *n* bits and *M* codewords that corrects a given maximum number of errors is NP-hard. For this reason the problem has been solved in the literature with heuristic techniques such as Simulated Annealing and Genetic Algorit ...

Keywords: heuristics, information theory, local search, parallelism

14 <u>Efficient checker processor design</u>

Saugata Chatterjee, Chris Weaver, Todd Austin
December 2000 **Proceedings of the 33rd annual ACM/IEEE international symposium on Microarchitecture**
Publisher: ACM Press
Full text available: pdf(153.00 KB)

ps(1.26 MB)   Additional Information: <u>full citation</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>
<u>Publisher Site</u>

15 <u>A proof of the security of quantum key distribution (extended abstract)</u>

Eli Biham, Michel Boyer, P. Oscar Boykin, Tal Mor, Vwani Roychowdhury
May 2000 **Proceedings of the thirty-second annual ACM symposium on Theory of computing**
Publisher: ACM Press
Full text available: pdf(968.70 KB)   Additional Information: <u>full citation</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

16 DIVA: a reliable substrate for deep submicron microarchitecture design

Todd M. Austin

November 1999 **Proceedings of the 32nd annual ACM/IEEE international symposium on Microarchitecture**

Publisher: IEEE Computer Society

Full text available: pdf(1.47 MB) Publisher Site   Additional Information: full citation, abstract, references, citings, index terms

Building a high-performance microprocessor presents many reliability challenges. Designers must verify the correctness of large complex systems and construct implementations that work reliably in varied (and occasionally adverse) operating conditions. To further complicate this task, deep submicron fabrication technologies present new reliability challenges in the form of degraded signal quality and logic failures caused by natural radiation interference.

17 Execution-driven simulation of multiprocessors: address and timing analysis

S. Dwarkadas, J. R. Jump, J. B. Sinclair

October 1994 **ACM Transactions on Modeling and Computer Simulation (TOMACS),** Volume 4 Issue 4

Publisher: ACM Press

Full text available: pdf(1.58 MB)   Additional Information: full citation, abstract, references, citings, index terms

This article describes and evaluates an efficient execution-driven technique for the simulation of multiprocessors that includes the simulation of system memory and that is driven by real program work loads. The technique produces correctly interleaved address traces at run-time without disk access overhead or hardware support, allowing accurate simulation of the effects of a variety of architectural alternatives on programs. We have implemented a simulator based on this technique that offe ...

Keywords: distributed systems, execution-driven simulation, parallel tracing, shared-memory multiprocessors

18 Piranha: a scalable architecture based on single-chip multiprocessing

Luiz André Barroso, Kourosh Gharachorloo, Robert McNamara, Andreas Nowatzyk, Shaz Qadeer, Barton Sano, Scott Smith, Robert Stets, Ben Verghese

May 2000 **ACM SIGARCH Computer Architecture News , Proceedings of the 27th annual international symposium on Computer architecture ISCA '00,** Volume 28 Issue 2

Publisher: ACM Press

Full text available: pdf(191.10 KB)   Additional Information: full citation, abstract, references, citings, index terms

The microprocessor industry is currently struggling with higher development costs and longer design times that arise from exceedingly complex processors that are pushing the limits of instruction-level parallelism. Meanwhile, such designs are especially ill suited for important commercial applications, such as on-line transaction processing (OLTP), which suffer from large memory stall times and exhibit little instruction-level parallelism. Given that commercial applications constitute by fa ...

19 Evaluating the memory overhead required for COMA architectures

T. Joe, J. L. Hennessy

April 1994 **ACM SIGARCH Computer Architecture News , Proceedings of the 21ST annual international symposium on Computer architecture ISCA '94,** Volume 22 Issue 2

Publisher: IEEE Computer Society Press, ACM Press

Full text available: pdf(1.31 MB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

Cache only memory architectures (COMA) have an inherent memory overhead due to the organization of main memory as a large cache called an attraction memory. This overhead consists of memory left unallocated for performance reasons as well as additional physical memory required due to the cache organization of memory. In this work, we examine the effect of data reshuffling and data replication on the memory overhead. Data reshuffling occurs when space needs to be allocated to store a remote memor ...

20 <u>General storage protection techniques: Ensuring data integrity in storage: techniques and applications</u>

Gopalan Sivathanu, Charles P. Wright, Erez Zadok

November 2005 **Proceedings of the 2005 ACM workshop on Storage security and survivability StorageSS '05**

Publisher: ACM Press

Full text available: pdf(217.83 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>index terms</u>

Data integrity is a fundamental aspect of storage security and reliability. With the advent of network storage and new technology trends that result in new failure modes for storage, interesting challenges arise in ensuring data integrity. In this paper, we discuss the causes of integrity violations in storage and present a survey of integrity assurance techniques that exist today. We describe several interesting applications of storage integrity checking, apart from security, and discuss the im ...

Keywords: file systems, intrusion detection, storage integrity

Results 1 - 20 of 61   Result page: **1**   <u>2</u>   <u>3</u>   <u>4</u>   <u>next</u>

ⓐ P❀RTAL

USPTO

THE ACM DIGITAL LIBRARY

▪ Feedback Report a problem Satisfaction survey

Terms used **error correction code**   Found **943** of **169,866**

Sort results by | relevance ▽ | ❖ Save results to a Binder   Try an Advanced Search
Display results | expanded form ▽ | ⓘ Search Tips   Try this search in The ACM Guide
                                         ☐ Open results in a new window

Results 1 - 20 of 200   Result page: **1** 2 3 4 5 6 7 8 9 10   next
Best 200 shown   Relevance scale ☐ ▢ ▤ ▥ ■

1   Error correcting codes, perfect hashing circuits, and deterministic dynamic dictionaries   ■
Peter Bro Miltersen
January 1998 **Proceedings of the ninth annual ACM-SIAM symposium on Discrete algorithms**
Publisher: Society for Industrial and Applied Mathematics
Full text available: 📄 pdf(1.01 MB)   Additional Information: full citation, references, citings, index terms

2   Linear-time encodable and decodable error-correcting codes   ■
Daniel A. Spielman
May 1995 **Proceedings of the twenty-seventh annual ACM symposium on Theory of computing**
Publisher: ACM Press
Full text available: 📄 pdf(993.36 KB)   Additional Information: full citation, references, citings, index terms

3   Error-correcting codes for semiconductor memories   ■
C. L. Chen
January 1984 **ACM SIGARCH Computer Architecture News , Proceedings of the 11th annual international symposium on Computer architecture ISCA '84,**
Volume 12 Issue 3
Publisher: ACM Press
Full text available: 📄 pdf(242.74 KB)   Additional Information: full citation, references, index terms

4   Multiclass learning, boosting, and error-correcting codes   ■
Venkatesan Guruswami, Amit Sahai
July 1999 **Proceedings of the twelfth annual conference on Computational learning theory**
Publisher: ACM Press
Full text available: 📄 pdf(1.30 MB)   Additional Information: full citation, references, citings, index terms

5

On the efficiency of local decoding procedures for error-correcting codes
Jonathan Katz, Luca Trevisan
May 2000 **Proceedings of the thirty-second annual ACM symposium on Theory of computing**
Publisher: ACM Press
Full text available: pdf(732.43 KB)   Additional Information: full citation, references, citings, index terms

6 Using a genetic algorithm to find good linear error-correcting codes
Kelly M. McGuire, Roberta Evans Sabin
February 1998 **Proceedings of the 1998 ACM symposium on Applied Computing**
Publisher: ACM Press
Full text available: pdf(470.83 KB)   Additional Information: full citation, references, index terms

Keywords: genetic algorithm, linear error correcting codes

7 Declustering using error correcting codes
C. Faloutsos, D. Metaxas
March 1989 **Proceedings of the eighth ACM SIGACT-SIGMOD-SIGART symposium on Principles of database systems**
Publisher: ACM Press
Full text available: pdf(616.35 KB)   Additional Information: full citation, abstract, references, citings, index terms

The problem examined is to distribute a binary Cartesian product file on multiple disks to maximize the parallelism for partial match queries. Cartesian product files appear as a result of some secondary key access methods, such as the multiattribute hashing [10], the grid file [6] etc.. For the binary case, the problem is reduced into grouping the 2n binary strings on n bits in m groups of unsimilar strings. The main i ...

8 Evolutionary computation and optimization (ECO): Solving the error correcting code problem with parallel hybrid heuristics
Enrique Alba, J. Francisco Chicano
March 2004 **Proceedings of the 2004 ACM symposium on Applied computing**
Publisher: ACM Press
Full text available: pdf(289.83 KB)   Additional Information: full citation, abstract, references, index terms

Some telecommunication systems can not afford the cost of repeating a corrupted message. Instead, the message should be somewhat "corrected" by the receiver. In these cases an *error correcting code* is suitable. The problem of finding an error correcting code of *n* bits and *M* codewords that corrects a given maximum number of errors is NP-hard. For this reason the problem has been solved in the literature with heuristic techniques such as Simulated Annealing and Genetic Algorit ...

Keywords: heuristics, information theory, local search, parallelism

9 A fuzzy commitment scheme
Ari Juels, Martin Wattenberg
November 1999 **Proceedings of the 6th ACM conference on Computer and communications security**
Publisher: ACM Press

Full text available: pdf(966.08 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

> We combine well-known techniques from the areas of error-correcting codes and cryptography to achieve a new type of cryptographic primitive that we refer to as a fuzzy commitment scheme. Like a conventional cryptographic commitment scheme, our fuzzy commitment scheme is both concealing and binding: it is infeasible for an attacker to learn the committed value, and also for the committer to decommit a value in more than one way. In a convent ...

10 <u>Cryptanalysis of private-key encryption schemes based on burst-error-correcting codes</u>

Hung-Min Sun, Shiuh-Pyng Shieh

January 1996 **Proceedings of the 3rd ACM conference on Computer and communications security**

Publisher: ACM Press

Full text available: pdf(329.04 KB)   Additional Information: <u>full citation</u>, <u>references</u>, <u>index terms</u>

11 <u>Faster solution of the key equation for decoding BCH error-correcting codes</u>

Victor Y. Pan

May 1997 **Proceedings of the twenty-ninth annual ACM symposium on Theory of computing**

Publisher: ACM Press

Full text available: pdf(1.15 MB)   Additional Information: <u>full citation</u>, <u>references</u>, <u>index terms</u>

12 <u>Performance analysis of DS-CDMA with slotted ALOHA random access for packet PCNs</u>

Zhao Liu, Magda El Zarki

February 1995 **Wireless Networks**, Volume 1 Issue 1

Publisher: Kluwer Academic Publishers

Full text available: pdf(1.56 MB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>, <u>review</u>

> In this paper, we present a discrete time Markov chain based analytical framework for the study of Direct-Sequence Code-Division-Multiple-Access (DS-CDMA) with slotted ALOHA random access protocols (DS-CDMA-S-ALOHA) for packet Personal Communications Networks (PCNs). It incorporates both the random access and the random errors associated with DS-CDMA-S-ALOHA protocols into a unified framework. The key feature is that it distinguishes between the two stages in the transmission process, namel ...

13 <u>Session 8 (brief announcements): Asynchronous interactive consistency and its relation with error-correcting codes</u>

Achour Mostefaoui, Sergio Rajsbaum, Michel Raynal

July 2002 **Proceedings of the twenty-first annual symposium on Principles of distributed computing**

Publisher: ACM Press

Full text available: pdf(102.60 KB)   Additional Information: <u>full citation</u>, <u>references</u>

14 <u>Complexity theory: Guest column: error-correcting codes and expander graphs</u>

Venkatesan Guruswami

September 2004 **ACM SIGACT News**, Volume 35 Issue 3

Publisher: ACM Press

Full text available: pdf(650.42 KB)　　Additional Information: full citation, abstract, references

The central algorithmic problem in coding theory is the construction of explicit error-correcting codes with good parameters together with fast algorithms for encoding a message and decoding a noisy received word into the correct message. Over the last decade, significant new developments have taken place on this problem using graph-based/combinatorial constructions that exploit the power of <i>expander graphs</i>. The role of expander graphs in theoretical computer science is by now ...

15 Extractors and pseudorandom generators

July 2001 **Journal of the ACM (JACM)**, Volume 48 Issue 4

Publisher: ACM Press

Full text available: pdf(181.64 KB)　　Additional Information: full citation, abstract, references, citings, index terms, review

We introduce a new approach to constructing extractors. Extractors are algorithms that transform a "e;weakly random"e; distribution into an almost uniform distribution. Explicit constructions of extractors have a variety of important applications, and tend to be very difficult to obtain.We demonstrate an unsuspected connection between extractors and pseudorandom generators. In fact, we show that every pseudorandom generator of a certain kind is an extractor.A pseudorandom generator const ...

Keywords: Error-correcting codes, extractors, pseudorandomness

16 Declustering of key-based partitioned signature files

Paolo Ciaccia, Paolo Tiberio, Pavel Zezula

September 1996 **ACM Transactions on Database Systems (TODS)**, Volume 21 Issue 3

Publisher: ACM Press

Full text available: pdf(2.58 MB)　　Additional Information: full citation, abstract, references, citings, index terms, review

Access methods based on signature files can largely benefit from possibilities offered by parallel environments. To this end, an effective declustering strategy that would distribute signatures over a set of parallel independent disks has to be combined with a synergic clustering which is employed to avoid searching the whole signature file while executing a query. This article proposes two parallel signature file organizations, Hamming Filter (HF **Keywords:** error correcting codes, information retrieval, parallel independent disks, partial match queries, performance evaluation, superimposed coding

17 Extracting all the randomness and reducing the error in Trevisan's extractors

Ran Raz, Omer Reingold, Salil Vadhan

May 1999 **Proceedings of the thirty-first annual ACM symposium on Theory of computing**

Publisher: ACM Press

Full text available: pdf(870.20 KB)　　Additional Information: full citation, references, citings, index terms

18 Session 11A: Extractors: optimal up to constant factors

Chi-Jen Lu, Omer Reingold, Salil Vadhan, Avi Wigderson

June 2003 **Proceedings of the thirty-fifth annual ACM symposium on Theory of computing**

Publisher: ACM Press

Full text available: pdf(246.08 KB)   Additional Information: full citation, abstract, references, citings, index terms

This paper provides the first explicit construction of extractors which are simultaneously optimal up to constant factors in *both* seed length and output length. More precisely, for every *n,k*, our extractor uses a random seed of length $O(log\ n)$ to transform any random source on *n* bits with (min-)entropy *k*, into a distribution on *(1-a)k* bits that is ε-close to uniform. Here a and ε can be taken to be any positive c ...

Keywords: condensers, locally decodable error-correcting codes, mergers, pseudorandomness, randomness extractors

19 Distributed fingerprints and secure information dispersal

Hugo Krawczyk
September 1993 **Proceedings of the twelfth annual ACM symposium on Principles of distributed computing**
Publisher: ACM Press
Full text available: pdf(1.03 MB)   Additional Information: full citation, references, citings, index terms

20 Error-resilient transmission of 3D models

Ghassan Alregib, Yucel Altunbasak, Jarek Rossignac
April 2005 **ACM Transactions on Graphics (TOG)**, Volume 24 Issue 2
Publisher: ACM Press
Full text available: pdf(10.73 MB)   Additional Information: full citation, abstract, references, index terms

In this article, we propose an error-resilient transmission method for progressively compressed 3D models. The proposed method is scalable with respect to both channel bandwidth and channel packet-loss rate. We jointly design source and channel coders using a statistical measure that (i) calculates the number of both source and channel coding bits, and (ii) distributes the channel coding bits among the transmitted refinement levels in order to maximize the expected decoded model quality. In orde ...

Keywords: 3D graphics compression, error resilience, joint source and channel coding, media streaming, priority encoding transmission, progressive transmission, unequal error protection, virtual reality over IP

Results 1 - 20 of 200   Result page: **1**   2   3   4   5   6   7   8   9   10   next

Useful downloads: Adobe Acrobat   QuickTime   Windows Media Player   Real Player

IEEE *Xplore* ®
RELEASE 2.1

Welcome United States Patent and Trademark Office

⊡Search Results

BROWSE   SEARCH   IEEE XPLORE GUIDE

Results for "(('error correction code')<in>metadata)"
Your search matched **206** of **1310010** documents.
A maximum of **100** results are displayed, **25** to a page, sorted by **Relevance** in **Descending** order.

☑e-mail

» Search Options

<u>View Session History</u>

<u>New Search</u>

» Key

IEEE JNL	IEEE Journal or Magazine
IEE JNL	IEE Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IEE CNF	IEE Conference Proceeding
IEEE STD	IEEE Standard

Modify Search

(('error correction code')<in>metadata)   **Search**

☐ Check to search only within this results set

Display Format: ◉ Citation   ○ Citation & Abstract

view selected items   <u>Select All</u> <u>Deselect All</u>   View: **1-25** | <u>26-5</u>

☐   1. **Iterative decoding for concatenated error correction coding in PRML chai**
Sawaguchi, H.; Mita, S.; Wolf, J.K.;
<u>Global Telecommunications Conference, 1999. GLOBECOM '99</u>
Volume 1B, 1999 Page(s):749 - 754 vol. 1b
Digital Object Identifier 10.1109/GLOCOM.1999.830165

<u>AbstractPlus</u> | Full Text: <u>PDF</u>(552 KB)   **IEEE CNF**
<u>Rights and Permissions</u>

☐   2. **Optimal two-level unequal error control codes for computer systems**
Ritthongpitak, T.; Kitakami, M.; Fujiwara, E.;
<u>Fault Tolerant Computing, 1996., Proceedings of Annual Symposium on</u>
25-27 June 1996 Page(s):190 - 199
Digital Object Identifier 10.1109/FTCS.1996.534606

<u>AbstractPlus</u> | Full Text: <u>PDF</u>(636 KB)   **IEEE CNF**
<u>Rights and Permissions</u>

☐   3. **High code rate error correction code design for partial response systems**
Miller, J.; Wolf, J.K.;
<u>Magnetics, IEEE Transactions on</u>
Volume 37, Issue 2, Part 1, March 2001 Page(s):704 - 707
Digital Object Identifier 10.1109/20.917604

<u>AbstractPlus</u> | Full Text: <u>PDF</u>(76 KB)   **IEEE JNL**
<u>Rights and Permissions</u>

☐   4. **An optimal noncoherent sequence estimation for time varying TDMA wire**
Patwary, M.N.; Rapajic, P.B.;
<u>Mobile Future, 2004 and the Symposium on Trends in Communications. Symp</u>
<u>IST Workshop on</u>
24-26 Oct. 2004 Page(s):118 - 121
Digital Object Identifier 10.1109/TIC.2004.1409513

<u>AbstractPlus</u> | Full Text: <u>PDF</u>(610 KB)   **IEEE CNF**
<u>Rights and Permissions</u>

☐   5. **A low power high speed error correction code macro using complementa**
transistor logic circuit
Wang, L.K.; Chen, H.H.;
<u>ASIC Conference and Exhibit, 1997. Proceedings., Tenth Annual IEEE Interna</u>
7-10 Sept. 1997 Page(s):17 - 20

Digital Object Identifier 10.1109/ASIC.1997.616970

AbstractPlus | Full Text: PDF(292 KB)   IEEE CNF
Rights and Permissions

6. **A new two-dimensional interleaving technique using successive packing**
Shi, Y.Q.; Xi Min Zhang;
Circuits and Systems I: Fundamental Theory and Applications, IEEE Transacti‹
Circuits and Systems I: Regular Papers, IEEE Transactions on]
Volume 49, Issue 6, June 2002 Page(s):779 - 789
Digital Object Identifier 10.1109/TCSI.2002.1010033

AbstractPlus | References | Full Text: PDF(360 KB)   IEEE JNL
Rights and Permissions

7. **An optical error-correction code with spectrum domain decoding**
Shu-Ming Tseng; Yibin Zheng; Bell, M.R.;
Communications, IEEE Transactions on
Volume 51, Issue 5, May 2003 Page(s):795 - 799
Digital Object Identifier 10.1109/TCOMM.2003.811415

AbstractPlus | References | Full Text: PDF(282 KB)   IEEE JNL
Rights and Permissions

8. **A 0.18-/spl mu/m CMOS front-end processor for a Blu-Ray Disc recorder ‹
PRML**
Goang Seog Choi; Joo Seon Kim; Hyun Jeong Park; Young Jun Ahn; Hyun Sc
Bae; In Sik Park; Dong Ho Shin;
Solid-State Circuits, IEEE Journal of
Volume 40, Issue 1, Jan. 2005 Page(s):342 - 350
Digital Object Identifier 10.1109/JSSC.2004.837933

AbstractPlus | Full Text: PDF(1224 KB)   IEEE JNL
Rights and Permissions

9. **Error-tolerant SPIHT image compression**
Collins, T.; Atkins, P.;
Vision, Image and Signal Processing, IEE Proceedings-
Volume 148, Issue 3, June 2001 Page(s):182 - 186
Digital Object Identifier 10.1049/ip-vis:20010376

AbstractPlus | Full Text: PDF(444 KB)   IEE JNL

10. **An unequal error protection method for progressively transmitted 3D mo**
AlRegib, G.; Altunbasak, Y.; Rossignac, J.;
Multimedia, IEEE Transactions on
Volume 7, Issue 4, Aug. 2005 Page(s):766 - 776
Digital Object Identifier 10.1109/TMM.2005.850981

AbstractPlus | Full Text: PDF(1160 KB)   IEEE JNL
Rights and Permissions

11. **A 4-Mbit DRAM with 16-bit concurrent ECC**
Yamada, T.; Kotani, H.; Matsushima, J.; Inoue, M.;
Solid-State Circuits, IEEE Journal of
Volume 23, Issue 1, Feb. 1988 Page(s):20 - 26
Digital Object Identifier 10.1109/4.251

AbstractPlus | Full Text: PDF(548 KB)   IEEE JNL
Rights and Permissions

12. **Performance comparison of antenna diversity and slow frequency hoppii
portable radio channel**
Chang, L.F.; Porter, P.T.;
Vehicular Technology, IEEE Transactions on
Volume 38, Issue 4, Nov. 1989 Page(s):222 - 229

Digital Object Identifier 10.1109/25.45485

AbstractPlus | Full Text: PDF(620 KB)   IEEE JNL
Rights and Permissions

13. **Performance of a coded band-limited spread-spectrum multiple-access s channel sensing**
Resheff, S.; Rubin, I.;
Selected Areas in Communications, IEEE Journal on
Volume 8, Issue 4, May 1990 Page(s):515 - 528
Digital Object Identifier 10.1109/49.54450

AbstractPlus | Full Text: PDF(832 KB)   IEEE JNL
Rights and Permissions

14. **Magneto-resistive head thermal asperity digital compensation**
Galbraith, R.L.; Kerwin, G.J.; Poss, J.M.;
Magnetics, IEEE Transactions on
Volume 28, Issue 5, Part 2, Sep 1992 Page(s):2731 - 2732
Digital Object Identifier 10.1109/20.179611

AbstractPlus | Full Text: PDF(160 KB)   IEEE JNL
Rights and Permissions

15. **Performance evaluation of multiple access techniques in land mobile sat**
Fakhouri, S.; Yongacoglu, A.;
Vehicular Technology, IEEE Transactions on
Volume 41, Issue 3, Aug. 1992 Page(s):226 - 230
Digital Object Identifier 10.1109/25.155969

AbstractPlus | Full Text: PDF(460 KB)   IEEE JNL
Rights and Permissions

16. **Protocol configuration and verification of an adaptive error control scher cellular networks**
Sato, T.; Kawabe, M.; Kato, T.; Fukasawa, A.;
Vehicular Technology, IEEE Transactions on
Volume 41, Issue 1, Feb. 1992 Page(s):69 - 76
Digital Object Identifier 10.1109/25.120147

AbstractPlus | Full Text: PDF(580 KB)   IEEE JNL
Rights and Permissions

17. **Development of DARC decoding LSI for high-speed FM subcarrier syster**
Suka, M.; Hirakoso, Y.; Takahashi, Y.; Kuroda, T.; Takada, M.; Fujiwara, M.; T Okada, S.;
Consumer Electronics, IEEE Transactions on
Volume 40, Issue 3, Aug 1994 Page(s):570 - 579
Digital Object Identifier 10.1109/30.320843

AbstractPlus | Full Text: PDF(876 KB)   IEEE JNL
Rights and Permissions

18. **Performance of hybrid multichannel AM/256-QAM video lightwave transn**
Ovadia, S.; Hongxing Dai; Chinlon Lin; Anderson, W.T.;
Photonics Technology Letters, IEEE
Volume 7, Issue 11, Nov. 1995 Page(s):1351 - 1353
Digital Object Identifier 10.1109/68.473495

AbstractPlus | Full Text: PDF(264 KB)   IEEE JNL
Rights and Permissions

19. **A new signal processing technique for d=1 channel codes**
Umemoto, M.;
Magnetics, IEEE Transactions on
Volume 31, Issue 2, Mar 1995 Page(s):1044 - 1049

Digital Object Identifier 10.1109/20.364782

AbstractPlus | Full Text: PDF(316 KB)   IEEE JNL
Rights and Permissions

20. **Hybrid AM-VSB/M-QAM multichannel video transmission over 120 km of mode fiber with cascaded erbium-doped fiber amplifiers**
Hongxing Dai; Ovadia, S.; Chinlon Lin;
Photonics Technology Letters, IEEE
Volume 8, Issue 12, Dec. 1996 Page(s):1713 - 1715
Digital Object Identifier 10.1109/68.544728

AbstractPlus | References | Full Text: PDF(308 KB)   IEEE JNL
Rights and Permissions

21. **Requirements and constraints for the design of smart photodetector arra oriented optical memories**
Schaffer, M.E.; Mitkas, P.A.;
Selected Topics in Quantum Electronics, IEEE Journal of
Volume 4, Issue 5, Sept.-Oct. 1998 Page(s):856 - 865
Digital Object Identifier 10.1109/2944.735772

AbstractPlus | References | Full Text: PDF(320 KB)   IEEE JNL
Rights and Permissions

22. **A channel model for characterization of the error data recovered from co**
Tehranchi, B.; Howe, D.G.;
Communications, IEEE Transactions on
Volume 46, Issue 7, July 1998 Page(s):841 - 845
Digital Object Identifier 10.1109/26.701294

AbstractPlus | References | Full Text: PDF(144 KB)   IEEE JNL
Rights and Permissions

23. **A modified concatenated coding scheme, with applications to magnetic (**
Fan, J.L.; Calderbank, A.R.;
Information Theory, IEEE Transactions on
Volume 44, Issue 4, July 1998 Page(s):1565 - 1574
Digital Object Identifier 10.1109/18.681333

AbstractPlus | References | Full Text: PDF(372 KB)   IEEE JNL
Rights and Permissions

24. **Smart photodetector array for page-oriented optical memory in 0.35-μm (**
Schaffer, T.E.; Mitkas, P.A.;
Photonics Technology Letters, IEEE
Volume 10, Issue 6, June 1998 Page(s):866 - 868
Digital Object Identifier 10.1109/68.681512

AbstractPlus | References | Full Text: PDF(132 KB)   IEEE JNL
Rights and Permissions

25. **Error propagation evaluation for RLL-constrained DFE read channels**
Krachkovsky, V.Yu.; Yuan Xing Lee; Mathew, G.; Bin Liu; Lin, M.Y.; Wood, R.\
Chwee;
Magnetics, IEEE Transactions on
Volume 34, Issue 1, Part 1, Jan. 1998 Page(s):147 - 152
Digital Object Identifier 10.1109/20.663504

AbstractPlus | Full Text: PDF(664 KB)   IEEE JNL
Rights and Permissions

IEEE *Xplore*
RELEASE 2.1

Welcome United States Patent and Trademark Office

⌂˸Search Results

BROWSE   **SEARCH**   **IEEE XPLORE GUIDE**

Results for "(('error correction code')<in>metadata)"
Your search matched **206** of **1310010** documents.
A maximum of **100** results are displayed, **25** to a page, sorted by **Relevance** in **Descending** order.

✉e-mail

» Search Options

View Session History

New Search

» Key

IEEE JNL	IEEE Journal or Magazine
IEE JNL	IEE Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IEE CNF	IEE Conference Proceeding
IEEE STD	IEEE Standard

Modify Search

((('error correction code')<in>metadata)   **Search**

☐ Check to search only within this results set

Display Format: ◉ Citation ◯ Citation & Abstract

view selected items   **Select All Deselect All**   View: 1-25 | 26-5

☐ **51. Adding robustness to geometrical attacks to a wavelet based, blind vide system**
Serdean, C.V.; Ambroze, M.A.; Tomlinson, M.; Wade, J.G.;
Multimedia and Expo, 2002. ICME '02. Proceedings. 2002 IEEE International (
Volume 1, 26-29 Aug. 2002 Page(s):557 - 560 vol.1
Digital Object Identifier 10.1109/ICME.2002.1035842

AbstractPlus | Full Text: PDF(365 KB)   **IEEE CNF**
Rights and Permissions

☐ **52. Combating geometrical attacks in a DWT based blind video watermarkin**
Serdean, C.V.; Ambroze, M.A.; Tomlinson, M.; Wade, G.;
Video/Image Processing and Multimedia Communications 4th EURASIP-IEEE
International Symposium on VIPromCom
16-19 June 2002 Page(s):263 - 266
Digital Object Identifier 10.1109/VIPROM.2002.1026666

AbstractPlus | Full Text: PDF(432 KB)   **IEEE CNF**
Rights and Permissions

☐ **53. On the performance of turbo product codes and LDPC codes over partial channels**
Jing Li; Kurtas, E.; Narayanan, K.R.; Georghiades, C.N.;
Communications, 2001. ICC 2001. IEEE International Conference on
Volume 7, 11-14 June 2001 Page(s):2176 - 2183 vol.7
Digital Object Identifier 10.1109/ICC.2001.937042

AbstractPlus | Full Text: PDF(732 KB)   **IEEE CNF**
Rights and Permissions

☐ **54. Peak-to-average power ratio reduction of an OFDM signal using data per embedded side information**
Jayalath, A.D.S.; Tellambura, C.;
Circuits and Systems, 2001. ISCAS 2001. The 2001 IEEE International Symp
Volume 4, 6-9 May 2001 Page(s):562 - 565 vol. 4
Digital Object Identifier 10.1109/ISCAS.2001.922299

AbstractPlus | Full Text: PDF(292 KB)   **IEEE CNF**
Rights and Permissions

☐ **55. Performance evaluation of TCP throughput on wireless cellular networks**
Miyoshi, M.; Sugano, M.; Murata, M.;

Vehicular Technology Conference, 2001. VTC 2001 Spring. IEEE VTS 53rd
Volume 3, 6-9 May 2001 Page(s):2177 - 2181 vol.3
Digital Object Identifier 10.1109/VETECS.2001.945082

AbstractPlus | Full Text: PDF(416 KB)   IEEE CNF
Rights and Permissions

56. Hard disk controller: the disk drive's brain and body
Jeppesen, J.; Allen, W.; Anderson, S.; Pilsl, M.;
Computer Design, 2001. ICCD 2001. Proceedings. 2001 International Confere
23-26 Sept. 2001 Page(s):262 - 267
Digital Object Identifier 10.1109/ICCD.2001.955038

AbstractPlus | Full Text: PDF(448 KB)   IEEE CNF
Rights and Permissions

57. On the asynchronous frequency hopping systems performance
Giovanardi, A.; Mazzini, G.;
Personal, Indoor and Mobile Radio Communications, 2001 12th IEEE Internati
on
Volume 2, 30 Sept.-3 Oct. 2001 Page(s):F-1 - F-5 vol.2
Digital Object Identifier 10.1109/PIMRC.2001.965283

AbstractPlus | Full Text: PDF(299 KB)   IEEE CNF
Rights and Permissions

**58. Efficient error correction code configurations for quasi-nonvolatile data r
DRAMs**
Katayama, Y.; Negishi, Y.; Morioka, S.;
Defect and Fault Tolerance in VLSI Systems, 2000. Proceedings. IEEE Interna
Symposium on
25-27 Oct. 2000 Page(s):201 - 209
Digital Object Identifier 10.1109/DFTVS.2000.887158

AbstractPlus | Full Text: PDF(312 KB)   IEEE CNF
Rights and Permissions

59. On the performance of parity codes in magnetic recording systems
Feng, W.; Vityaev, A.; Burd, G.; Nazari, N.;
Global Telecommunications Conference, 2000. GLOBECOM '00. IEEE
Volume 3, 27 Nov.-1 Dec. 2000 Page(s):1877 - 1881 vol.3
Digital Object Identifier 10.1109/GLOCOM.2000.891959

AbstractPlus | Full Text: PDF(268 KB)   IEEE CNF
Rights and Permissions

60. A study of enhancing the robustness of watermark
Ching-Tang Hsieh; Yu-Lung Lu; Chih-Ping Luo; Feng-Jen Kuo;
Multimedia Software Engineering, 2000. Proceedings. International Symposiur
11-13 Dec. 2000 Page(s):325 - 327
Digital Object Identifier 10.1109/MMSE.2000.897230

AbstractPlus | Full Text: PDF(224 KB)   IEEE CNF
Rights and Permissions

61. Effect of interleaving on a Markov channel
Rancurel, T.; Roviras, D.; Conan, J.; Castanie, F.;
Acoustics, Speech, and Signal Processing, 2000. ICASSP '00. Proceedings. 2
International Conference on
Volume 5, 5-9 June 2000 Page(s):2661 - 2664 vol.5
Digital Object Identifier 10.1109/ICASSP.2000.861024

AbstractPlus | Full Text: PDF(212 KB)   IEEE CNF
Rights and Permissions

62. Simulation of multiple-bit soft errors induced by cosmic ray neutrons in I

☐ Tosaka, Y.; Satoh, S.;
Simulation of Semiconductor Processes and Devices, 2000. SISPAD 2000. 20
Conference on
6-8 Sept. 2000 Page(s):265 - 268
Digital Object Identifier 10.1109/SISPAD.2000.871259

AbstractPlus | Full Text: PDF(220 KB)   IEEE CNF
Rights and Permissions

☐ 63. **Wireless image transmission using multiple-description based concaten**
Sachs, D.G.; Anand, R.; Ramchandran, K.;
Data Compression Conference, 2000. Proceedings. DCC 2000
28-30 March 2000 Page(s):569
Digital Object Identifier 10.1109/DCC.2000.838216

AbstractPlus | Full Text: PDF(32 KB)   IEEE CNF
Rights and Permissions

☐ 64. **Fault-tolerant refresh power reduction of DRAMs for quasi-nonvolatile da**
Katayama, Y.; Stuckey, E.J.; Morioka, S.; Wu, Z.;
Defect and Fault Tolerance in VLSI Systems, 1999. DFT '99. International Syn
1-3 Nov. 1999 Page(s):311 - 318
Digital Object Identifier 10.1109/DFTVS.1999.802898

AbstractPlus | Full Text: PDF(92 KB)   IEEE CNF
Rights and Permissions

☐ 65. **Yield enhancement considerations for a single-chip multiprocessor syste**
embedded DRAM
Rudack, M.; Niggemeyer, D.;
Defect and Fault Tolerance in VLSI Systems, 1999. DFT '99. International Syn
1-3 Nov. 1999 Page(s):31 - 39
Digital Object Identifier 10.1109/DFTVS.1999.802866

AbstractPlus | Full Text: PDF(96 KB)   IEEE CNF
Rights and Permissions

☐ 66. **Study and subjective evaluation on MPEG-4 narrowband CELP coding ur**
communication conditions
Ozawa, K.; Nomura, T.; Serizawa, M.; Ehara, H.; Yoshida, K.; Tana, N.;
Speech Coding Proceedings, 1999 IEEE Workshop on
20-23 June 1999 Page(s):129 - 131
Digital Object Identifier 10.1109/SCFT.1999.781507

AbstractPlus | Full Text: PDF(240 KB)   IEEE CNF
Rights and Permissions

☐ 67. **Soft-output decoding for concatenated error correction in high-order PRI**
Sawaguchi, H.; Mita, S.;
Communications, 1999. ICC '99. 1999 IEEE International Conference on
Volume 3, 6-10 June 1999 Page(s):1632 - 1637 vol.3
Digital Object Identifier 10.1109/ICC.1999.765508

AbstractPlus | Full Text: PDF(576 KB)   IEEE CNF
Rights and Permissions

☐ 68. **Architecture for a smart Reed-Solomon decoder**
Boutillon, E.; Dehamel, A.;
Circuits and Systems, 1999. 42nd Midwest Symposium on
Volume 1, 8-11 Aug. 1999 Page(s):236 - 239 vol. 1
Digital Object Identifier 10.1109/MWSCAS.1999.867251

AbstractPlus | Full Text: PDF(292 KB)   IEEE CNF
Rights and Permissions

69. **A nonlinear iterative beamforming technique for wireless communicatior**

Sellathurai, M.; Haykin, S.;
Signals, Systems, and Computers, 1999. Conference Record of the Thirty-Thir
Conference on
Volume 2, 24-27 Oct. 1999 Page(s):957 - 961 vol.2
Digital Object Identifier 10.1109/ACSSC.1999.831851

AbstractPlus | Full Text: PDF(376 KB)   IEEE CNF
Rights and Permissions

70. **Concatenated error correction coding for high-order PRML channels**
Sawaguchi, H.; Kondou, M.; Kobayashi, N.; Mita, S.;
Global Telecommunications Conference, 1998. GLOBECOM 98. The Bridge to
Integration. IEEE
Volume 5, 8-12 Nov. 1998 Page(s):2694 - 2699 vol.5
Digital Object Identifier 10.1109/GLOCOM.1998.776474

AbstractPlus | Full Text: PDF(352 KB)   IEEE CNF
Rights and Permissions

71. **Performance analysis of coherent reverse link in W-CDMA system emplo
array antennas**
Jin-Kyu Han; Cheol Mun; Jae-Ho Yoo; Han-Kyu Park;
Antennas and Propagation Society International Symposium, 1998. IEEE
Volume 1, 21-26 June 1998 Page(s):368 - 371 vol.1
Digital Object Identifier 10.1109/APS.1998.699156

AbstractPlus | Full Text: PDF(124 KB)   IEEE CNF
Rights and Permissions

72. **A reduced complexity detector/decoder for multiuser communications wi**
Shama, Y.; Vojcic, B.R.;
Information Theory, 1998. Proceedings. 1998 IEEE International Symposium c
16-21 Aug. 1998 Page(s):284
Digital Object Identifier 10.1109/ISIT.1998.708889

AbstractPlus | Full Text: PDF(76 KB)   IEEE CNF
Rights and Permissions

73. **WATM operation optimisation based on effect of FEC code rate and ARQ**
Hiew, P.L.; Zukerman, M.; Gitlits, M.;
Vehicular Technology Conference, 1998. VTC 98. 48th IEEE
Volume 3, 18-21 May 1998 Page(s):2542 - 2546 vol.3
Digital Object Identifier 10.1109/VETEC.1998.686218

AbstractPlus | Full Text: PDF(368 KB)   IEEE CNF
Rights and Permissions

74. **Image transmission using arithmetic coding based continuous error dete**
Kozintsev, I.; Chou, J.; Ramchandran, K.;
Data Compression Conference, 1998. DCC '98. Proceedings
30 March-1 April 1998 Page(s):339 - 348
Digital Object Identifier 10.1109/DCC.1998.672162

AbstractPlus | Full Text: PDF(608 KB)   IEEE CNF
Rights and Permissions

75. **HiBRIC-MEM, a memory controller for PowerPC based systems**
Porrmann, M.; Landmann, J.; Marks, K.-M.; Ruckert, U.;
EUROMICRO 97. 'New Frontiers of Information Technology'., Proceedings of I
EUROMICRO Conference
1-4 Sept. 1997 Page(s):653 - 657
Digital Object Identifier 10.1109/EURMIC.1997.617396

AbstractPlus | Full Text: PDF(944 KB)   IEEE CNF
Rights and Permissions

Help   Contact Us   Privacy & :

Indexed by
Inspec

IEEE *Xplore* ®
RELEASE 2.1

Welcome United States Patent and Trademark Office

░□░**Search Results**

BROWSE   **SEARCH**   **IEEE XPLORE GUIDE**

Results for "(('error correction code')<in>metadata)"   ☑e-mail
Your search matched **206** of **1310010** documents.
A maximum of **100** results are displayed, **25** to a page, sorted by **Relevance** in **Descending** order.

» Search Options

View Session History

New Search

» Key

IEEE JNL	IEEE Journal or Magazine
IEE JNL	IEE Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IEE CNF	IEE Conference Proceeding
IEEE STD	IEEE Standard

Modify Search

(('error correction code')<in>metadata)   **Search**

☐ Check to search only within this results set

Display Format: ◉ Citation   ○ Citation & Abstract

↵ **view selected items**   **Select All** **Deselect All**   View: 1-25 | 26-5

☐ 76. **Cellular Digital Packet Data (CDPD) equipment: some practical design is:**
Bump, G.D.; Rappaport, T.S.;
Universal Personal Communications, 1996. Record., 1996 5th IEEE Internatioi on
Volume 2, 29 Sept.-2 Oct. 1996 Page(s):801 - 805 vol.2
Digital Object Identifier 10.1109/ICUPC.1996.562686

AbstractPlus | Full Text: PDF(432 KB)   IEEE CNF
Rights and Permissions

☐ 77. **Random parity coding (RPC)**
Hershey, J.; Tiemann, J.;
Communications, 1996. ICC 96, Conference Record, Converging Technologie Applications. 1996 IEEE International Conference on
Volume 1, 23-27 June 1996 Page(s):122 - 126 vol.1
Digital Object Identifier 10.1109/ICC.1996.540257

AbstractPlus | Full Text: PDF(208 KB)   IEEE CNF
Rights and Permissions

☐ 78. **Acoustic video-signal transmission system for autonomous underwater ᵛ**
Kojima, J.; Shirasaki, Y.;
Autonomous Underwater Vehicle Technology, 1996. AUV '96., Proceedings of Symposium on
2-6 June 1996 Page(s):348 - 353
Digital Object Identifier 10.1109/AUV.1996.532434

AbstractPlus | Full Text: PDF(568 KB)   IEEE CNF
Rights and Permissions

☐ 79. **Good error detection codes satisfy the expurgated bound**
Hashimoto, T.;
Information Theory, 1995. Proceedings., 1995 IEEE International Symposium ᵛ
17-22 Sept. 1995 Page(s):343
Digital Object Identifier 10.1109/ISIT.1995.550330

AbstractPlus | Full Text: PDF(88 KB)   IEEE CNF
Rights and Permissions

☐ 80. **Block coded modulation based ARQ schemes with blocks combining**
Al-Sanie, A.M.;
Computers and Communications, 1995. Proceedings., IEEE Symposium on

27-29 June 1995 Page(s):346 - 350
Digital Object Identifier 10.1109/SCAC.1995.523686

AbstractPlus | Full Text: PDF(352 KB)   IEEE CNF
Rights and Permissions

81. **Type-II hybrid ARQ schemes with block coded modulation**
Al-Sanie, K.M.;
Networks, 1995. Theme: 'Electrotechnology 2000: Communications and Netwc
conjunction with the] International Conference on Information Engineering., Pro
IEEE Singapore International Conference on
3-7 July 1995 Page(s):463 - 467
Digital Object Identifier 10.1109/SICON.1995.526330

AbstractPlus | Full Text: PDF(360 KB)   IEEE CNF ·
Rights and Permissions

82. **Communication protocol for virtual cellular network (VCN)**
Hwa-Jong Kim; Won-Young Jeong;
Personal, Indoor and Mobile Radio Communications, 1995. PIMRC'95. 'Wirele
the Information Superhighway'., Sixth IEEE International Symposium on
Volume 1, 27-29 Sept. 1995 Page(s):343 - 347 vol.1
Digital Object Identifier 10.1109/PIMRC.1995.476910

AbstractPlus | Full Text: PDF(388 KB)   IEEE CNF
Rights and Permissions

83. **Unbiased reliability with one bit error correction**
Koo, D.Y.;
Reliability and Maintainability Symposium, 1988. Proceedings., Annual
26-28 Jan. 1988 Page(s):350 - 355
Digital Object Identifier 10.1109/ARMS.1988.196475

AbstractPlus | Full Text: PDF(412 KB)   IEEE CNF
Rights and Permissions

84. **A system for multichannel PCM sound broadcasting via satellite and son
results**
Kawai, N.; Kameda, K.; Yoshino, T.;
Global Telecommunications Conference, 1988, and Exhibition. 'Communicatio
Information Age.' Conference Record, GLOBECOM '88., IEEE
28 Nov.-1 Dec. 1988 Page(s):153 - 157 vol.1
Digital Object Identifier 10.1109/GLOCOM.1988.25828

AbstractPlus | Full Text: PDF(364 KB)   IEEE CNF
Rights and Permissions

85. **Session: optical communication systems**
Information Theory, 1988. Abstracts of Papers., 1988 IEEE International Symp
19-24 June 1988 Page(s):198 - 200
Digital Object Identifier 10.1109/ISIT.1988.22297

AbstractPlus | Full Text: PDF(176 KB)   IEEE CNF
Rights and Permissions

86. **Performance comparison of antenna diversity and slow frequency hoppii
portable radio channel**
Chang, L.F.; Porter, P.T.;
Vehicular Technology Conference, 1989 IEEE 39th
1-3 May 1989 Page(s):464 - 469 vol.2
Digital Object Identifier 10.1109/VETEC.1989.40115

AbstractPlus | Full Text: PDF(468 KB)   IEEE CNF
Rights and Permissions

87. **A coded modulation design for the INMARSAT geostationary GLONASS** ;

☐ Stein, B.; Tsang, W.;
Military Communications Conference, 1991. MILCOM '91, Conference Record
Communications in a Changing World'., IEEE
4-7 Nov. 1991 Page(s):321 - 325 vol.1
Digital Object Identifier 10.1109/MILCOM.1991.258476

AbstractPlus | Full Text: PDF(260 KB)   IEEE CNF
Rights and Permissions

☐ 88. **Group 3 facsimile communication scheme in transparent mode on digital**
Sato, T.; Kawabe, M.; Kato, T.; Fukasawa, A.;
Circuits and Systems, 1991., IEEE International Sympoisum on
11-14 June 1991 Page(s):958 - 961 vol.2
Digital Object Identifier 10.1109/ISCAS.1991.176523

AbstractPlus | Full Text: PDF(264 KB)   IEEE CNF
Rights and Permissions

☐ 89. **Producing soft-decision information at the output of a class IV partial res
detector**
Knudson, K.J.; Wolf, J.K.; Milstein, L.B.;
Communications, 1991. ICC 91, Conference Record. IEEE International Confe
23-26 June 1991 Page(s):820 - 824 vol.2
Digital Object Identifier 10.1109/ICC.1991.162473

AbstractPlus | Full Text: PDF(348 KB)   IEEE CNF
Rights and Permissions

☐ 90. **Adaptive ARQ using complementary coding and signal editing**
Ng, W.-H.;
Military Communications Conference, 1992. MILCOM '92, Conference Record
'Communications - Fusing Command, Control and Intelligence'., IEEE
11-14 Oct. 1992 Page(s):589 - 593 vol.2
Digital Object Identifier 10.1109/MILCOM.1992.244010

AbstractPlus | Full Text: PDF(388 KB)   IEEE CNF
Rights and Permissions

☐ 91. **An FM multiplex broadcasting system for traffic information services**
Takada, M.; Kuroda, T.; Moriyama, S.; Isobe, T.; Yamada, O.;
Vehicle Navigation and Information Systems Conference, 1993., Proceedings
12-15 Oct. 1993 Page(s):344 - 347
Digital Object Identifier 10.1109/VNIS.1993.585645

AbstractPlus | Full Text: PDF(320 KB)   IEEE CNF
Rights and Permissions

☐ 92. **Throughput of a FHMA system for variable rate coding and interference ε**
Woerner, B.D.; Wardhana, A.I.;
Tactical Communications Conference, 1994. Vol. 1. Digital Technology for the
Communicator., Proceedings of the 1994
10-12 May 1994 Page(s):167 - 178
Digital Object Identifier 10.1109/TCC.1994.472130

AbstractPlus | Full Text: PDF(652 KB)   IEEE CNF
Rights and Permissions

☐ 93. **Development of high compression HDTV digital codec**
Matsumoto, S.; Hamada, T.; Koga, K.; Murakami, H.;
Telecommunications, 1995. Fifth IEE Conference on
26-29 Mar 1995 Page(s):100 - 104

AbstractPlus | Full Text: PDF(296 KB)   IEE CNF

☐ 94. **Cache and memory error detection, correction, and reduction techniques
servers and workstations**

Slayman, C.W.;
Device and Materials Reliability, IEEE Transactions on
Volume 5, Issue 3, Sept. 2005 Page(s):397 - 404
Digital Object Identifier 10.1109/TDMR.2005.856487

AbstractPlus | Full Text: PDF(304 KB)   IEEE JNL
Rights and Permissions

95. **Error Correction Code Performance on HF, Troposcatter, and Satellite Ch**
Brayer, K.;
Communications, IEEE Transactions on [legacy, pre - 1988]
Volume 19, Issue 5, Part 1, Oct 1971 Page(s):781 - 789

AbstractPlus | Full Text: PDF(832 KB)   IEEE JNL
Rights and Permissions

96. **Optimization of the Processing Gain of an FSK-FH System**
Milstein, L.; Pickholtz, R.; Schilling, D.;
Communications, IEEE Transactions on [legacy, pre - 1988]
Volume 28, Issue 7, Jul 1980 Page(s):1062 - 1079

AbstractPlus | Full Text: PDF(632 KB)   IEEE JNL
Rights and Permissions

97. **Development of an Error-Correction Method for Data Packet Multiplexed**
Yamada, O.;
Communications, IEEE Transactions on [legacy, pre - 1988]
Volume 35, Issue 1, Jan 1987 Page(s):21 - 31

AbstractPlus | Full Text: PDF(1376 KB)   IEEE JNL
Rights and Permissions

98. **Extremely low error rate digital recording with a helical scan recorder**
Baugh, R.; Bromley, D.; Spenner, B.;
Magnetics, IEEE Transactions on
Volume 22, Issue 5, Sep 1986 Page(s):1179 - 1181

AbstractPlus | Full Text: PDF(552 KB)   IEEE JNL
Rights and Permissions

99. **Good error-detection codes satisfying the expurgated bound**
Hashimoto, T.;
Information Theory, IEEE Transactions on
Volume 41, Issue 5, Sept. 1995 Page(s):1347 - 1353
Digital Object Identifier 10.1109/18.412681

AbstractPlus | Full Text: PDF(467 KB)   IEEE JNL
Rights and Permissions

100. **Robust modem and coding techniques for FM hybrid ICOC DAB**
Kroeger, B.; Cammarata, D.;
Broadcasting, IEEE Transactions on
Volume 43, Issue 4, Dec. 1997 Page(s):412 - 420
Digital Object Identifier 10.1109/11.664024

AbstractPlus | Full Text: PDF(1196 KB)   IEEE JNL
Rights and Permissions

View: 1-25 | 26-5

Help   Contact Us   Privacy & :

Google

Web   Images   Groups   News   Froogle   Local   **more »**

"error correction code"   **Search**   Advanced Search
                                          Preferences

Web   Results **1** - **10** of about **136,000** for **"error correction code"** . (0.59 seconds)

Error-Correcting Code -- From MathWorld

An error-correcting code is an algorithm for expressing a sequence of
numbers such that any errors which are introduced can be detected and
corrected ...
mathworld.wolfram.com/Error-CorrectingCode.html - 26k - Jan 31, 2006 -
Cached - Similar pages

Error Correction Codes

There's an **error correction code** that separates the bits holding the original
value ... If there is a 1-bit **error correction code** of b bits for n symbols, ...
burtleburtle.net/bob/math/errorcor.html - 8k - Cached - Similar pages

Error correction and detection - Wikipedia, the free encyclopedia

From Wikipedia, the free encyclopedia. (Redirected from Error correction). Jump to:
navigation, search. It has been suggested that this article or section ...
en.wikipedia.org/wiki/Error_correction - 36k - Jan 31, 2006 - Cached - Similar pages

ECC - a Whatis.com definition - see also: **error correction code** ...

News and advice for network managers and administrators.
searchnetworking.techtarget.com/ sDefinition/0,,sid7_gci212028,00.html - 43k - Jan 31,
2006 - Cached - Similar pages

Error Correction Code (ECC) (483)

has led to an **error-correction code** (ECC) for data transmission systems that are more
efficient than current methods in use. ECCs are used in all forms of ...
www.mdatechnology.net/techsearch.asp?articleid=483 - 21k - Cached - Similar pages

STMicroelectronics | **Error Correction Code** in Single Level Cell ...

STMicroelectronics is a global leader in developing and delivering System-on-Chip (SoC)
and semiconductor solutions ac ross the spectrum of microelectronics ...
www.st.com/stonline/products/literature/an/10123.htm - 44k - Cached - Similar pages

error correction code - definition of **error correction code** by the ...

Definition of **error correction code** in the Online Dictionary. Meaning of **error correction
code**. What does **error correction code** mean? **error correction code** ...
www.thefreedictionary.com/error+correction+code - 25k - Cached - Similar pages

Multichannel **Error Correction Code** Decoder

Wagner, PK; and Ivancic, WD: Multichannel **Error Correction Code** Decoder. AIAA Paper
94-1025 (NASA TM-106331), 1994. Find out more about Lewis' Digital ...
www.grc.nasa.gov/WWW/RT1995/5000/5650s.htm - 5k - Cached - Similar pages

Error Correction

For example, let's consider the specific (3,1) **error correction code** described by the
following coding table and, more concisely, by the succeeding matrix ...
cnx.rice.edu/content/m0095/latest/ - 19k - Cached - Similar pages

Diagnostic Error Codes

257, ECC 2-9, **Error Correction Code**. Between two to nine errors have been ... 258, ECC

10+, Error Correction Code. At least ten errors have been detected. ...
support.wdc.com/techinfo/general/errorcodes.asp - 51k - Jan 31, 2006 -
Cached - Similar pages

Try your search again on Google Book Search

Goooooooooogle ▶

Result Page: 1 2 3 4 5 6 7 8 9 10   **Next**

| "error correction code" | Search |

Search within results | Language Tools | Search Tips | Dissatisfied? Help us improve

Google Home - Advertising Programs - Business Solutions - About Google

©2006 Google

Linking Technology to Application

PROGRAM

| Tech Search | MDA Update | Special Reports | Tech News | Meetings & Events | Media Resources | Links | Hom |

Tech Search

Need a technology solution?

Then you've come to the right place. Search MDA-funded technology abstracts and find the technology that is right for you.

Spinoff Technology: # 483

🖶 Print   ✉ Email

Error Correction Code (ECC)
Science Dynamics Corporation (Cherry Hill, NJ)

Summary:

A BMDO SBIR Phase I contract for virtual memory redundancy management
has led to an error-correction code (ECC) for data transmission systems that are more efficient than current methods in use. ECCs are used in all forms of digital circuitry and communications, from CD players to satellite links. The ECC was originally developed by GORCA Systems Incorporated (GSI; Cherry Hill, NJ) and then used as the basis for the formation of GORCA Memory Systems, Inc. (GMS; Cherry Hill, NJ). In 1999 that version of the ECC was purchased by Science Dynamics Corporation (SIDY; Cherry Hill, NJ). The ECC has since been enhanced by a combination of engineering efforts from SIDY and Technology Enhancement Partners, LLC (TEP; Medford, NJ). The first product of this joint effort is to be packaged in an error-correction chip for use in wireless local area networks and marketed by SIDY. Additional products are being developed by SIDY and TEP together and separately.

Technology Description:

In the mid 1990s, GORCA Memory Systems (GMS; Cherry Hill, NJ) was formed to create and market products using an error-correction code (ECC) that was developed by Walter Helbig, a consultant to GORCA Systems Incorporated (GSI; Cherry Hill, NJ). The system was purchased by Science Dynamics Corporation (SIDY; Cherry Hill, NJ) when GMS went out of business. SIDY and Technology Enhancement Partners, LLC (TEP; Cherry Hill, NJ) are jointly refining the ECC and developing new products and applications using it.

Error correcting is essential for error-free transmission of digital data. Whenever one digital component is tasked with sending bits to a second component (a "component" may be any digital device such as a cellular phone, computer network card, or computer memory that transmits data to the computer's processor), the possibility

for the data set becoming corrupted in route arises. Error correction is made available by adding checksum bits to the packets of data that move between components. These bits allow the receiving component to determine if the packet of data arrived intact. Unfortunately, checksum bits also add overhead to each data packet ("overhead" is the percentage of checksum bits to data bits per packet). The larger the overhead, the lengthier the packets, and, therefore, the longer it takes for the entire set of data to be transmitted.

GMS's patented ECC is a form of a computable rotational code that can operate as a single burst-error correcting, multiple burst-error detecting code. Whereas most industry standard schemes, such as the Reed-Solomon (RS) code, operate at a fixed symbol length and, in typical implementations require an overhead of approximately eight percent, this code is designed to operate in the same applications with a much smaller overhead, typically on the order of one percent. The arithmetic process used with this code is similar to that used in the National Security Agency's secure hash algorithm (SHA-1). As such, the computational rate is much higher than it is for the RS code while the cost of implementation is anticipated to be considerably less. Further, the code provides modification correction capability in addition to modification detection capability at costs approximately equal to that of the SHA-1 code. Because there are virtually an infinite number of variations to this code, users can implement the single version or multiple versions that optimally match the intended application.

This ECC can facilitate further efficiency in virtual memory-addressing settings, because part of the ECC can be implemented on the virtual memory translation unit itself. Plus, because of its compact design, this code can be implemented on a single field-programmable gate array, which is a type of programmable logic chip, thereby saving production costs over error-correcting schemes that require more elaborate hardware.

BMD Origins:

In 1992, GSI completed a BMDO SBIR Phase I on virtual memory redundancy management for reliable multiprocessing. BMDO needed to facilitate complex, low-error rate data communications between satellite and ground systems. The original design was for a block-error correction for high-capacity, high-performance, solid-state memory systems.

Spinoff Applications:

Although error corrections are used in all forms of digital circuitry, from CD players to computers, efficient error correction is particularly valued in wireless communications, where throughput is already more limited than land-based lines. So everything from satellite communication links to cell phones could benefit from efficient error correction.

One possible application is in wireless local area networks (WLANs). These are similar to office networks of computers but, instead of the computers being attached to one another by cables, they communicate by radio signals. WLANS are useful for companies in old buildings where cable installation is costly or prohibitive. They might also be used with offices that are mobile. According to the Cahners In-Stat Group research firm, the corporate WLAN market may be worth $2.2 billion by 2004.

Another application area is in digital signatures. A digital signature is code that identifies the sender of an electronic document. Digital signatures were created to provide the electronic equivalent to written signatures. That is to say, they can guarantee the identity of the signer of the document. Because of this, digital signatures are predicted to become a cornerstone of the e-commerce marketplace, which requires authentication to conduct business. This ECC can be used in a digital signature system and can provide an additional benefit of error-correcting documents as they are transmitted.

Commercialization:

The present version of the error-detecting and correcting system is now jointly owned by SIDY and TEP. The first product that employs the technology is a forward error-correcting (FEC) chip that SIDY is now marketing. GMS' principal investigator Walter Helbig, who founded TEP, is making further improvements to the ECC for applications for both companies.

SIDY and TEP have designed the forward error correcting (FEC) chip using this technology, called HSBC (Helbig-Shrinivasan Block Code) Burst Error Detection and Correction (HEDCtm). This chip could be used in commercial satellite terminals to replace the larger, more expensive, and less efficient Reed-Solomon FEC (which has about an eight-percent communication overhead).

TE is proposing that the HEDC chip (or variants of it) be accepted by the IEEE 802 standards bodies for the newer communications systems. The value of this code and its implementation can be seen in that in a typical WLAN one in every 100,000 packets transmitted will be received with errors. SIDY and TEP have shown that HEDC can, in this application, correct all these errors. Plus, they have shown that the HEDC chip is less costly than the Reed-Solomon components for the same system, as its efficient design can be implemented entirely on one integrated circuit. SIDY estimates that its HEDC chip will cost approximately a tenth of the full set of components for the Reed-Solomon unit. The HEDC FEC chip can work either in half-duplex or full-duplex mode, and has a capability of over 20 megabits per second, with gigabyte-level throughput possible. Further, the HEDC chip can automatically switch between handling short command packets (of up to 512 data bytes in length) and longer information packets (of over 512 data bytes in length).

SIDY may also employ the ECC in the global telecommunications network being built by SIDY for Cascadent Communications, Ltd. (Washington, D.C.). SIDY is a supplier of key technologies to this company, which has an ambitious plan for building a seamless Internet protocol-based global communications facility for large to medium corporate users with international communications requirements.

TEP is working toward employing the ECC as a part of secure computer systems. It is working with the McLean, VA-based Amron Corporation on an SBIR Phase I contract for the Navy SPAWAR program. The work involves the creation of a Security Level B-2 computer design. The company is also working on ways to implement the ECC into the efforts of the Trusted Computing Platform Alliance (http://www.trustedpc.org), a coalition of computer vendors interested in embedding stronger security functionality within computers. In these applications, the ECC can be implemented in the system's basic input/output system (BIOS) setting to insure that documents that are called up on a computer haven't been tampered with since the last authorized use. The ECC can also restore corrupted documents to their former state.

Company Profile:

GORCA Memory Systems went out of business in 1999. The intellectual property of the error correcting system was sold to SIDY. SIDY, incorporated in 1973, specializes in telecommunications systems, including intelligent call processing platforms which provide telecommunications service capabilities to the public switched telephone network. The company has recently focused on providing products for packet-based data networks. TEP has spearheaded the development of refinements to the ECC to add capabilities not available with other ECCs. TEP, working jointly with SIDY, has led the development of HEDC FEC chip which SIDY will produce and market.

Contact Information: Walter Helbig(original principal investigator at GORCA Systems, Inc. for the BMDO SBIR Phase I project)
Technology Enhancement Partners, LLC
51 Haddonfield Road Suite 340
Cherry Hill NJ 08002-4810

Tel:609-654-6865
Fax:Same
E-mail: walter_helbig@email.msn.com

Fred Miller
Science Dynamics Corporation
1919 Springdale Road
Cherry Hill NJ 08003
Tel:856-424-0068 x604
Fax:856-751-7361
E-mail: sales@scidyn.com
Web Site: http://www.scidyn.com/

Approved for Public Release
MDA (9 JAN 2001)

Note: The Ballistic Missile Defense Organization (BMDO) and the Strategic Defense Initiative Organization (SDIO) are predecessors of the Missile Defense Agency (MDA).

Disclaimer: The views expressed in this article are those of the author and do not reflect the official policy or position of the Department of Defense or the U.S. Government.

| **Spinoffs by Firm** | **Spinoffs by Location** |

Quick Search

[_____] **GO**
Search our comprehensive database of
tech briefs and newsletter articles for MDA
technology with commercial potential.

Suggested Searches

[Select a Topic   ▼]
View our list of suggested keyword search
terms categorized by technology area.

Home | **Contact** | **Free Help** | **Search** | **Links** | **Products** | **Forums**

Search

[ ]

Tips | **Search**

Other Pages

Home
Site map
Computer help
Dictionary
News
Q&A
What's new

Tools

E-mail this page
Print Preview
Edit this page

ECC

Short for **Error-Correction Code**, ECC is a method of detecting and then correcting errors within the computer memory.

When purchasing computer memory you may notice ECC or NON-ECC memory, or Error-Correction Code Memory / Non-Error Correction Code Memory. Generally, NON-ECC memory will always be cheaper.

> Additional help and information about computer memory can be found on our memory help page.

Also see: Memory definitions, Parity

INDEX   ?

Category:
Dictionary

Related Pages:
E - Definitions

RESOLVED   ?

Were you able to locate the answer to your questions?

- Yes
- No

△ TOP

Other Pages: Main Page | Computer help | Contact | Dictionary | Products
Games | Art/Images | HTML Help | Virus Info | Network
Link to Computer Hope | Bookmark Computer Hope

Search: [   ] | search

| Home | Contact | Free Help | Search | Links | Products | Forums |

Search

Tips | Search

Other Pages

Home
Site map
Computer help
Dictionary
News
Q&A
What's new

Tools

E-mail this page
Print Preview
Edit this page

Memory definitions

\# | A | B | C | D | E | F | G | H | I | J | K | L | M
N | O | P | Q | R | S | T | U | V | W | X | Y | Z

Number
640KB barrier
A
Absolute address
Allocation
B
Ball Grid Array
Bank
BEDO DRAM
BGA
Block
Bubble memory
Buffer
C
C-RIMM
Cache
Cache On A STick
CAS
CF
COAST
Column Access Strobe
CompactFlash
Composite
Conventional memory
Core
D
DDR
DDR2
Deallocation
Device address
Devicehigh
DIMM
Disk cache
Direct address

INDEX   ?

Category:
Dictionary

**Related
Pages:**
Memory Help

RESOLVED   ?

Were you able
to locate the
answer to your
questions?

- Yes
- No

Direct Memory Access
DMA
DMAC
DOS protected mode interface
Double Data Rate
DRAM
Dynamic allocation
Dynamic Random Access Memory
Dynamic relocation
Dynamic storage

E

ECC
EDO
EEPROM
EMM
EMS
EPROM
Expanded memory
Extended memory

F

Fast page
Flash memory
FPM RAM
FRAM
Frame buffer

G

Currently no listings

H

HMA

I

Input buffer
Interleave

J

JEDEC SDRAM
Jump drive

K

Currently no listings

L

Leak
LIM
LIM 4.0
LIM-EMS
Loader
Loadhigh
Local memory

M

MB
MDRAM
Megabyte
Memory
Memory capacity
Memory cartridge
Memory chips
Memory leak
Memory management
Memory manager
MMU

N

Noncomposite
Non-volatile memory
Null pointer
NVRAM

O

Output buffer

P

PAE
Page
PC Memory slot
Physical address
Physical Address Extensions
Physical memory
Pipe
PRAM
Primary storage device
Programmable ROM
PROM
Protected mode

Q

Currently no listings

R

RAM
RAM cache
RAM card
RAM chip
RAM disk
Random Access Memory
RAS
RDRAM
Refresh
Refresh cycle
RIMM
Roll in
ROM

ROM card
RSL

S
Scratch
SDRAM
SGRAM
Shadow memory
Shared memory
SIMM
SIPP
SO-DIMM
SO-RIMM
SOJ DRAM
Solid-state memory
SRAM
Static allocation
Storage devices

T
Tag
Transient

U
UMA
UMB
Upper Memory Area
Upper Memory Blocks

V
VCACHE
VCPI
Video memory
Virtual address
Virtual memory
VRAM
VMS
Volatile memory

W
WRAM

X
Currently no listings

Y
Currently no listings

Z
Zero out
Zero wait state

TOP

Other Pages: <u>Main Page</u> | <u>Computer help</u> | <u>Contact</u> | <u>Dictionary</u> |
<u>Products</u>
<u>Games</u> | <u>Art/Images</u> | <u>HTML Help</u> | <u>Virus Info</u> | <u>Network</u>
<u>Link to Computer Hope</u> | <u>Bookmark Computer Hope</u>

Search: [   ] | search |

<u>Copyright 1998-2006 by Computer Hope (tm). All rights reserved</u>
<u>Legal Disclaimer</u> | <u>ESD Warning</u> | <u>Privacy Statement</u>

Be More Organized
Your to-dos, notes, images, and files online. Easy, fast, and free.
www.backpackit.com

| Home | Contact | Free Help | Search | Links | Products | Forums |

Search

[search box]

| Tips | Search |

Other Pages

Home
Site map
Computer help
Dictionary
News
Q&A
What's new

Tools

E-mail this page
Print Preview
Edit this page

Parity

Basic method of checking data for errors during a transmission or on a data storage mechanism. Parity works by setting a parity <u>bit</u> to an even or odd number. If the <u>binary</u> stream contains eight 0's or 1's, for example, we know that this is an even number, however if the parity bit indicates that this number should be an odd number we would be able to easily determine that the data is corrupt.

Also see: CRC, ECC, Error control, Mark parity, No-parity, Parity bit, Parity Check, Space parity

INDEX [?]

Category:
Dictionary

Related Pages:
P - Definitions

RESOLVED [?]

Were you able to locate the answer to your questions?

- Yes
- No

[▲ TOP]

Other Pages: Main Page | Computer help | Contact | Dictionary | Products
Games | Art/Images | HTML Help | Virus Info | Network
Link to Computer Hope | Bookmark Computer Hope

Search: [search box] | search |